Faculty CV

1. **Name**
   Jibran Khan Yousafzai

2. **Academic Rank**
   Assistant Professor, Full-time

3. **Degrees**
   Ph.D. (Electronic Engineering) – 2010, King's College London, UK.
   M.Sc. (Digital Signal Processing) – 2006, King's College London, UK.
   B.S. (Computer System Engineering) – 2004, Ghulam Ishaq Khan Institute of Engineering Sciences and Technology, Pakistan.

4. **Service at this Institution**
   July 2011 – present, Assistant Professor of Electrical and Computer Engineering

5. **Professional Registration**
   PEC *– Pakistan Engineering Council*

6. **Selected Publications**

   I. Damaj, J. Yousafzai, H. Mouftah, "Future trends in connected and autonomous vehicles: enabling communication and processing technologies" – *IEEE ACCESS, Vol. 10, pages: 42334 – 42345, 2022.*

   I. Damaj, J. Yousafzai, "Effective assessment of student outcomes in computer engineering programs using a minimalistic framework" – *International Journal of Engineering Education, Vol. 35 (1A), pages: 59 – 75, 2019.*

   A. Zaher , J. Yousafzai, "An adaptive control approach to securely transmit colored images using chaos-based cryptography" – *The 62nd IEEE Midwest Symposium on Circuits and Systems, pages: 953 – 956, 2019.*

   I. Damaj, J. Yousafzai "A maximum mutuality model for continuous improvement in engineering departments" – *IEEE Global Engineering Education Conference EDUCON, pages: 994 – 998, 2018.*

7. **Membership in Professional Societies**

   IEEE *– Institute of Electrical and Electronic Engineers, IEEE Signal Processing Society and IEEE Robotics Society*
   IET *– Institution of Engineering and Technology*

8. **Patents, Honors and awards**

   Best Paper Award, IEEE EDUCON Conference, 2015

9. **Academic Courses (since 2018)**

Digital Signal Processing
Signals and Systems
Signals and Systems Laboratory
Embedded Systems Design
Computer Organization and Architecture
Digital Logic Design
Digital Logic Design Laboratory
Engineering Economics
Internship in Computer/Electrical Engineering
Senior Capstone Design I
Senior Capstone Design II

10. **Committees**
    a) Department

    Member, ECE Assessment and Curriculum Committee, Fall 2017 – Present
    Chair, ECE Project Evaluation Committee, Fall 2013 – Spring 2018
    Member, ECE Project Evaluation Committee, Fall 2018 – Present
    Organizer of ECE Exhibition and Seminar Series, 2018-19
    Member, ECE Industrial Advisory Board, Fall 2015 – Spring 2018
    Member, ECE Student Liaison Committee, Fall 2016 – Spring 2018
    Member, ECE Student Advisory Board, Fall 2018 – Present
    Member, ECE Data Collection Committee for the ABET Accreditation of the Computer Engineering Program, Spring 2012 – Spring 2018
    Member, ECE Adhoc Curriculum Committee, Fall 2018
    Member, ECE Ad hoc Committee for Engineering Mathematics, Fall 2011

    b) College level

    Member, College (CEAS) Academic Quality Committee, Fall 2020 –
    Member, College (CAS) Assessment Committee, Fall 2016 – Spring 2018

    c) Institution level

    Member, Student Code of Conduct Board, Fall 2020 –
    Member, University Computing & Technology Committee, Fall 2019 – Spring 2020
    Member, University Curriculum Committee, Fall 2012 – Spring 2015
    Member, University Grade Appeal Committee, Fall 2014 – Fall 2016
    Member, University Founder's Award Committee, Spring 2016
    Member, University Grade Distribution Committee, Spring 2012